The step numbers in this data sheet refer to those in the laboratory exercise instructions.

Step 2:

Table 1. *I*D vs. *V*GS, *V*DS=4V

Important: Do Not Let ID Exceed 45 mA

|  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | Nominal *I*D (mA) | | | | | | | | | | |
| Parameter | 0.1 | 0.2 | 0.4 | 0.7 | 1 | 2 | 4 | 7 | 10 | 20 | 40 |
| Measured *I*D (mA) |  |  |  |  |  |  |  |  |  |  |  |
| Measured *V*GS (volts) |  |  |  |  |  |  |  |  |  |  |  |

Table 2. *I*D vs. *V*DS, *V*GS=

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | Nominal *V*DS (V) | | | | | | | | |
| Parameter | 1.0 | 2.0 | 3.0 | 4.0 | 5.0 | 6.0 | 7.0 | 8.0 | 9.0 |
| Measured  *V*DS (V) |  |  |  |  |  |  |  |  |  |
| Measured  *I*D (mA) |  |  |  |  |  |  |  |  |  |

Table 3. *I*D vs. *V*DS, *V*GS=

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | Nominal *V*DS (V) | | | | | | | | |
| Parameter | 1.0 | 2.0 | 3.0 | 4.0 | 5.0 | 6.0 | 7.0 | 8.0 | 9.0 |
| Measured  *V*DS (V) |  |  |  |  |  |  |  |  |  |
| Measured  *I*D (mA) |  |  |  |  |  |  |  |  |  |

Step 3:

Table 4. Logic circuit 1 Measurements.

|  |  |  |
| --- | --- | --- |
| *V*1 (V) | *V*2 (V) | *V*out (V) |
| 0 | 0 |  |
| 0 | 5 |  |
| 5 | 0 |  |
| 5 | 5 |  |

Table 5. Logic circuit 2 Measurements.

|  |  |  |
| --- | --- | --- |
| *V*1 (V) | *V*2 (V) | *V*out (V) |
| 0 | 0 |  |
| 0 | 5 |  |
| 5 | 0 |  |
| 5 | 5 |  |

Other observations: