

Rilke Dietche

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Objective

Employment as an entry-level engineer in research and development.

Education

The University of Texas—Austin GPA: 3.9/4.0

Electrical and Computer Engineering

Bachelor of Science degree expected August 20XX

Related Courses: Digital system design using VHDL, operating system development using MC68340, computer-aided VLSI design using Magic, and DLX RISC microprocessor design using Compass.

Skills

Proficient in assembly language (MC6800 family), C++, and VHDL. Proficient in UNIX and system administration. Skilled in simulation tools including Workview, PSPICE, and Compass.

Experience

09/04 – 12/04 *Engineering Co-op, National Systems, Austin, Texas*

- Analyzed and documented customer problems
- Designed and verified hardware parts
- Tested Windows 7 device driver

09/03 – 12/03 *Assistant System Administrator, University of Texas—Austin*

- Administrated a network with over 40 users
- Upgraded operating system from SunOS to Solaris
- Designed homepage for the Telecommunication Laboratory

09/02 – 12/02 *Engineering Co-op, National Systems, Austin, Texas*

Developed and implemented verification plan for new General Purpose Interface Bus hardware product and software application

09/01 – 07/02 *Grader, The University of Texas—Austin*

Assisted professor in grading student homework in entry-level circuit theory class

Honors and Activities

Recipient of Engineering Scholar Award

Member of Tau Beta Pi Engineering Society

Member of Eta Kappa Nu Electrical Engineering Honor Society

References

Available on request